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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Applicant(s): Robert F. Wallace
Title: Semiconductor Package Using Terminals Formed on a
Conductive Layer of a Circuit Board
Application No.: 09/866,022 Filing Date: May 24, 2001
Examiner: Roy Karl Potter Group Art Unit: 2822
Docket No.: SNDK.112US2 Conf. No.: 6172

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CONTINUED EXAMINATION (RCE)

Dear Sir:

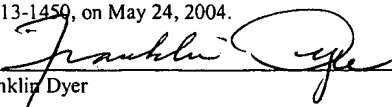
This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

Please consider the Petition Under 37 C.F.R. § 1.313(c) and Supplemental Information Disclosure Statement, which are being filed herewith. The RCE fee required under 37 C.F.R. § 1.17(e) is authorized in an accompanying transmittal letter.


Please contact the undersigned attorney with any questions concerning this request or the above-identified patent application.

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope address to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 24, 2004.


Franklin Dyer

Respectfully submitted,


Gerald P. Parsons
Reg. No. 24,486